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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Toshio Takayama et al

Group Art Unit: 2811

Serial No.: 10/735,886

Examiner: Nitin Parekh

Filed: December 16, 2003

P.T.O. Confirmation No.: 5693

For: SEMICONDUCTOR DEVICE HAVING A MULTILAYER
INTERCONNECTION STRUCTURE AND FABRICATION PROCESS
THEREOF

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: November 9, 2004

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated October 6, 2004, for one month from November 6, 2004 to December 6, 2004.

Attached please find a check in the amount of \$110.00, to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, KRATZ, QUINTOS, HANSON & BROOKS, LLP

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PATENT TRADEMARK OFFICE